

EAST Search History**EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	313	(706/13).OCLS.	USPAT; USOCR	OR	OFF	2010/02/12 18:00
L2	173	TRIMBERGER STEPHEN	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L3	1	"20060235814"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L4	110	evolvable hardware	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L5	0	consensus and L4	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L6	110	(evolvable hardware) or (evolv\$5 with3 hardware) or (evolv\$5 with3 (field programmable gate arrays or FPGAS))	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L7	279988	consensus or scor\$3 or vot\$3	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L8	29	L6 and L7	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L9	1	consensus adj2 based adj3 evaluation	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L10	1	bitwise and L8	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L11	0	consensus based adj3 evaluation	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L12	32	consensus adj3 evaluation	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L13	3	evolvable hardware and vot\$3	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L14	5646	(genetic or evolution) (programming or algorithm)	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L15	173	TRIMBERGER STEPHEN	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00

L16	3059	(consensus or voting or outlier) adj3 (scheme\$1 or method \$2 or approach\$2 or model\$1)	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L17	123	L14 and L16	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L18	69021	evolvable or program \$4 adj3 (hardware or circuit\$1 or array\$1)	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L19	1	L17 and L18	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L20	110	(evolvable hardware) or (evolv\$5 with3 hardware) or (evolv\$5 with3 (field programmable gate arrays or FPGAS))	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L21	0	L17 and L20	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L22	2	"20030088458"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L23	13	"6389588"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L24	109	decision fusion	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L25	1	"20040128219"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L26	2	"20040143524"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L27	5040	genetic algorithm	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L28	4120	cash flow	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L29	67	L27 and L28	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L30	1958	objective functions	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L31	67	L29 and L29	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L32	11	L29 and L30	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L33	313	(706/13).COLS.	USPAT; USOCR	OR	OFF	2010/02/12 18:00
L34	173	TRIMBERGER STEPHEN	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00

L35	1	"20060235814"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L36	110	evolvable hardware	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L37	0	consensus and L36	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L38	110	(evolvable hardware) or (evolv\$5 with3 hardware) or (evolv\$5 with3 (field programmable gate arrays or FPGAS))	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L39	279988	consensus or scor\$3 or vot\$3	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L40	29	L38 and L39	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L41	1	consensus adj2 based adj3 evaluation	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L42	1	bitwise and L40	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L43	0	consensus based adj3 evaluation	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L44	32	consensus adj3 evaluation	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L45	3	evolvable hardware and vot\$3	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L46	5646	(genetic or evolution) (programming or algorithm)	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L47	173	TRIMBERGER STEPHEN	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L48	3059	(consensus or voting or outlier) adj3 (scheme\$1 or method \$2 or approach\$2 or model\$1)	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L49	123	L46 and L48	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L50	69021	evolvable or program \$4 adj3 (hardware or circuit\$1 or array\$1)	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L51	1	L49 and L50	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00

L52	110	(evolvable hardware) or (evolv\$5 with3 hardware) or (evolv\$5 with3 (field programmable gate arrays or FPGAS))	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L53	0	L49 and L52	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L54	2	"20030088458"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L55	13	"6389588"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L56	109	decision fusion	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L57	1	"20040128219"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L58	2	"20040143524"	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L59	5040	genetic algorithm	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L60	4120	cash flow	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L61	67	L59 and L60	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L62	1958	objective functions	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L63	67	L61 and L61	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00
L64	11	L61 and L62	US-PGPUB; USPAT	ADJ	OFF	2010/02/12 18:00

EAST Search History (Interference)

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2/12/2010 6:03:43 PM

C:\Documents and Settings\lwong3\My Documents\EAST\Workspaces\10644162
Consensus as an evaluation function for evolvable hardware.wsp